

DP-652 US

#2 Pre Amt - 1
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R. Stokes

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Morishita, Y.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: AN INPUT/OUTPUT PROTECTION DEVICE FOR A SEMICONDUCTOR
INTEGRATED CIRCUIT

Assistant Commissioner of Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to examination on the merits and calculation of the filing fee, please amend the
above-identified application as follows:

IN THE CLAIMS:

Claim 4, line 2, delete "or 3".

Claim 5, lines 1-2, delete "one of claims 1 to 4" and insert --claim 1--.

Claim 6, lines 1-2, delete "one of claims 1 to 5" and insert --claim 1--.

Claim 7, lines 1-2, delete "one of claims 1 to 5" and insert --claim 1--.

Claim 8, lines 1-2, delete "one of claim 6 or 7" and insert --claim 6--.

Claim 9, lines 1-2, delete "one of claim 7 or 8" and insert --claim 7--.

Claim 10, line 2, delete "one of claim 7 or 8" and insert --claim 7--.

Claim 11, lines 1-2, delete "one of claims 1 to 10" and insert --claim 1--.

Claim 12. Lines 1-2, delete "one of claims 1 to 10" and insert --claim 1--.

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